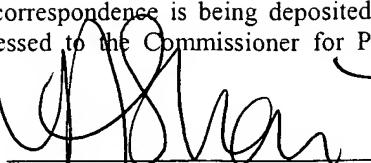




PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

February 13, 2004


Ayesha J. Shaikh

Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ralph James

Attorney Docket No.: 501297.01

Serial No. : 10/645,288

Group Art Unit : 2186

Filed : August 20, 2003

Examiner : Not Yet Assigned

Title : METHOD AND SYSTEM FOR CAPTURING AND BYPASSING MEMORY
TRANSACTIONS IN A HUB-BASED MEMORY SYSTEM

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. A copy of the non-patent literature reference, as required under 37 C.F.R. § 1.98(a)(2), is enclosed. Copies of the cited U.S. patents and U.S. patent application publications will not be submitted herewith in accordance with the waiver by the Office of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for U.S. national patent applications filed after June 30, 2003. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

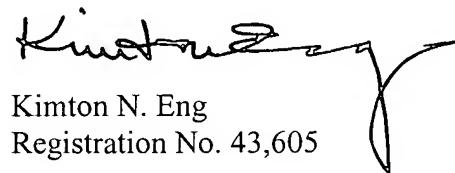
I hereby certify that no item set forth on the attached form PTO-1449 was cited in a communication from a foreign patent office in a counterpart foreign application or, to my

knowledge, after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



Kimton N. Eng
Registration No. 43,605

KNE:ajs

Enclosures:

Postcard
Form PTO-1449
Cited Reference (1)

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FORM PTO-1449 (REV.7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 501297.01	APPLICATION NO. 10/645,288
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		APPLICANT(S) Ralph James			
		FILING DATE August 20, 2003	GROUP ART UNIT 2186	O P E J C P FEB 18 2004 PATENT & TRADEMARK	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,818,844	10/06/98	Singh et al.	370	463	
	AB	6,272,609 B1	08/07/01	Jeddeloh	711	169	
	AC	6,477,592 B1	11/05/02	Chen et al.	710	52	
	AD	6,523,092 B1	02/18/03	Fanning	711	134	
	AE	6,523,093 B1	02/18/03	Bogin et al.	711	137	
	AF	6,622,227 B2	09/16/03	Zumkehr et al.	711	167	
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	AH	2002/0144064 A1	10/03/02	Fanning	711	144	
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	AJ	2003/0229770 A1	12/11/03	Jeddeloh	711	213	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AK							
	AL							
	AM							
	AN							
	AO							

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

AP	Intel, "Intel 840 Chipset: 82840 Memory Controller Hub (MCH)", Datasheet, October 1999, pp. 1-178.
AQ	

EXAMINER	DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).